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PVA227.001AUS

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Tsuruto Matsui ) Group Art Unit 2133  
Appl. No. : 09/853,998 )  
Filed : May 12, 2001 )  
For : PATTERN GENERATOR FOR )  
SEMICONDUCTOR TEST SYSTEM )  
Examiner : Matthew C. Dooley )

REQUEST FOR APPROVAL OF DRAWING CHANGES

Hon. Commissioner  
of Patents and Trademarks  
Alexandria, VA 22313-1450

Dear Sir:

This is to request for approval of drawing changes in which a "Prior Art" label is added to Figures 1 and 2a as shown in red ink to overcome the objection to the drawings. Replacement sheet of the amended drawings are also enclosed.

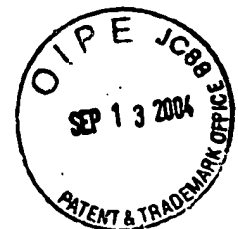
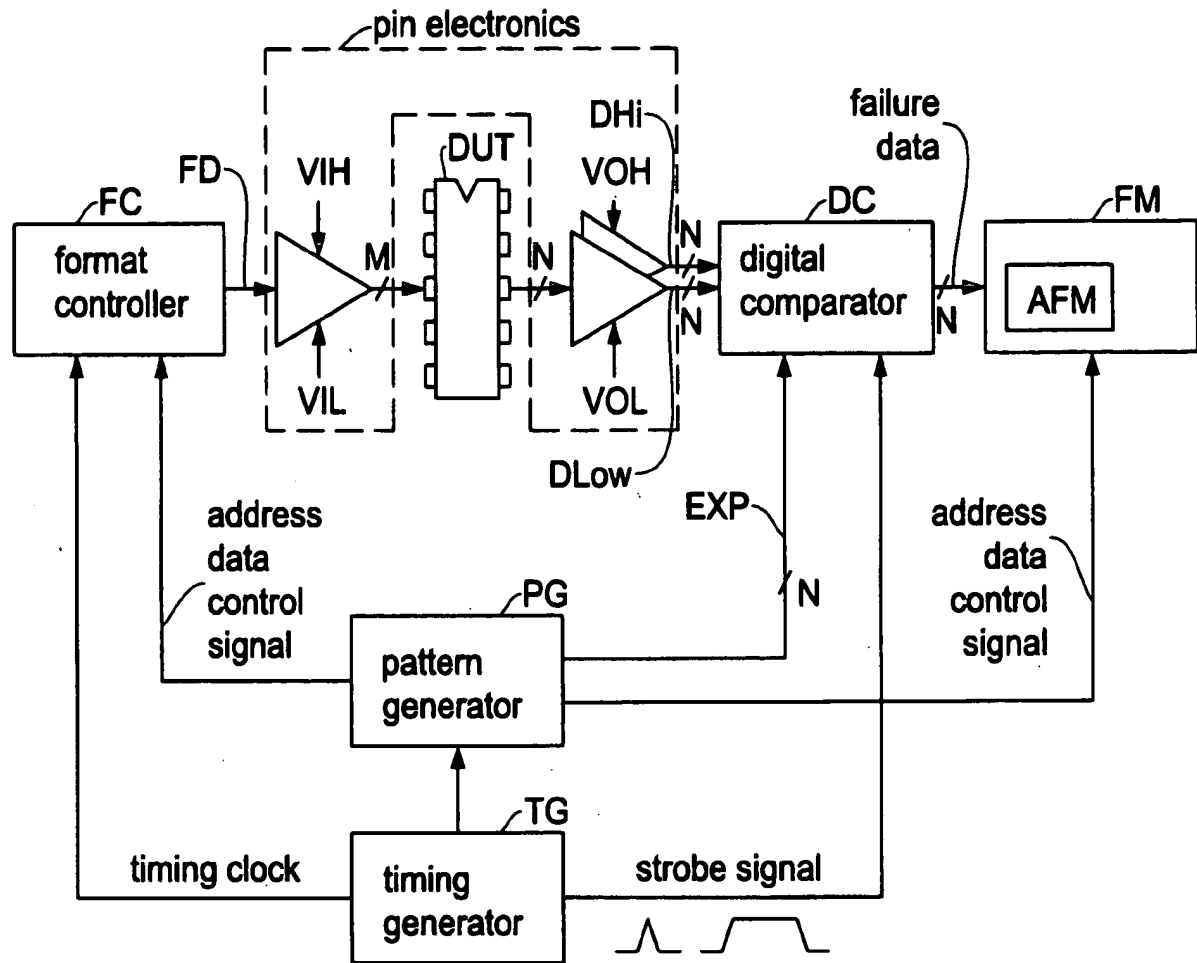
Respectfully submitted,

MURAMATSU & ASSOCIATES

Dated: 9/8/04

By: *Yasuo Muramatsu*  
Yasuo Muramatsu  
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FIG. 1 (Prior Art)



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Ch

FIG. 2A (Prior Art)

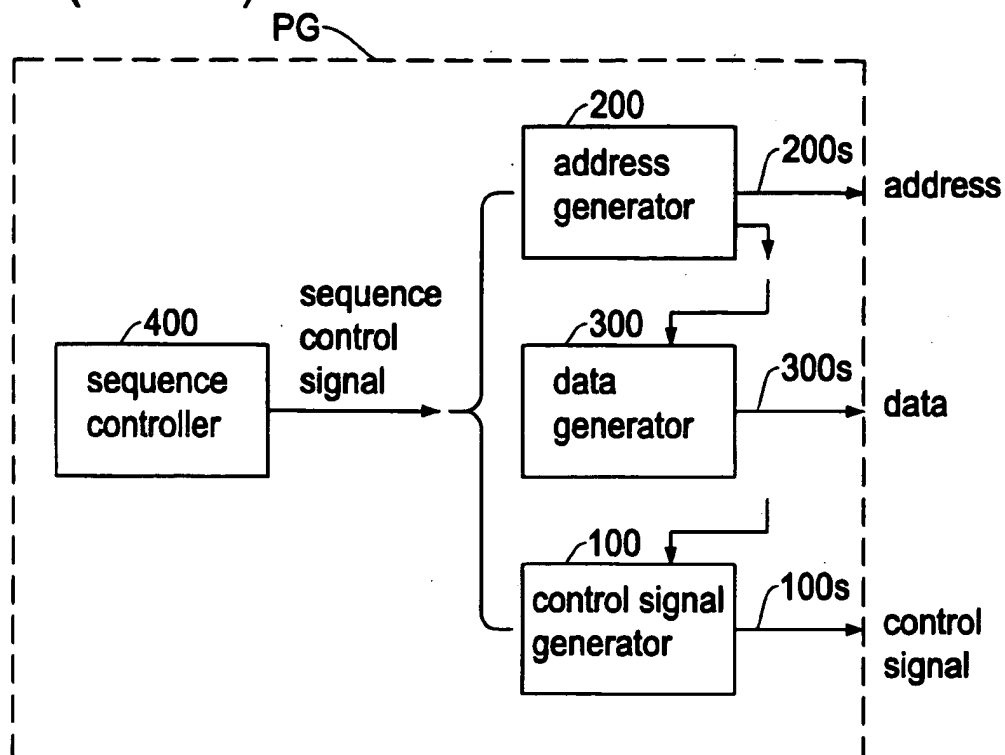


FIG. 2B

